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-40V P-Channel Enhancement Mode MOSFET



The XPX80P04FD uses advanced trench technology to

provide excellent $R_{\text{DS}(\text{ON})}\text{,}$ low gate charge and

operation with gate voltages as low as 4.5V. This device

is suitable for use as a

Battery protection or in other Switching application.

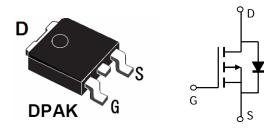
Application

Lithium battery protection

Wireless impact

Mobile phone fast charging

 $V_{DS} = -40V, I_D = -80A$ RDS(ON)=6.0mΩ (typ) @ VGS=-10V RDS(ON)=8mΩ (typ) @ VGS=-4.5V



Absolute Maximum Ratings ($T_A = 25^{\circ}C$, unless otherwise noted)

Parameter	Symbol	Value	Unit		
Drain-Source Voltage	Vds	-40	V		
Gate-Source Voltage	Vgs	±20	V		
Continuous Drain Current	Tc=25°C	1-	-80	Δ	
	Tc=100°C	lD	-50.6	A	
Pulsed Drain Current ¹	Ідм	-320	А		
Single Pulse Avalanche Energy ²	EAS	101.25	mJ		
Total Power Dissipation Tc=25°C		PD	85	w	
Operating Junction and Storage Temperature F	ТЈ , ТЅТС	-55 to 150	°C		

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient ³	Reja	54	°C/W
Thermal Resistance from Junction-to-Case	Rejc	1.54	°C/W



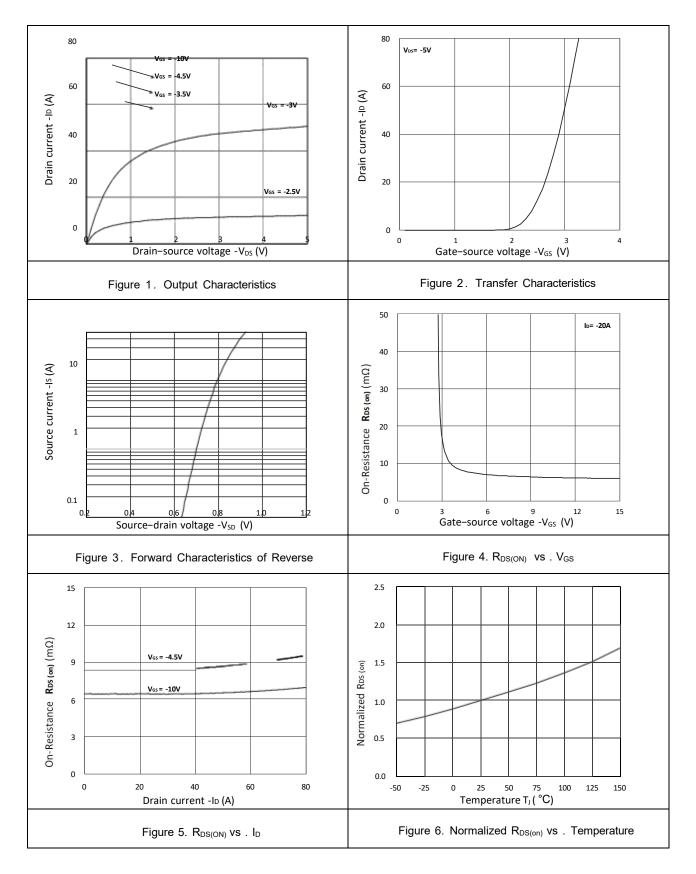
Electrical Characteristics (T_J = 25°C, unless otherwise notedl)

Parameter		Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static Characteristics			1		1	1	
Drain-Source Breakdown Vo	ltage	V(BR)DSS	Vgs = 0V, Id = -250µA	-40	-	-	V
Gate-body Leakage current		I _{GSS}	$V_{DS} = 0V$, $V_{GS} = \pm 20V$	-	-	±100	nA
Zero Gate Voltage Drain	TJ=25°C			-	-	-1	рА
Current	TJ=100°C	- Idss	$V_{DS} = -40V, V_{GS} = 0V$	-	-	-100	
Gate-Threshold Voltage		VGS(th)	Vds = Vgs , Id = -250µA	-1.0	-1.6	-2.5	V
Ducin Courses on Desistance	1	_	Vgs = -10V, Id = -20A	_ 6.0 8.2		8.2	— mΩ
Drain-Source on-Resistance ⁴		RDS(on)	Vgs = -4.5V, Id = -15A	-	8.0	11	
Forward Transconductance ⁴		g fs	VDS= -10V, ID= -20A	-	104	-	s
Dynamic Characteristics	5					1	
Input Capacitance		Ciss		-	5295	-	
Output Capacitance		Coss	VDS = -20V, VGS =0V, f =1MHz	-	430	_	pF
Reverse Transfer Capacitance		Crss		-	385	_	
Gate Resistance		Rg	f =1MHz	-	4.3	-	Q
Switching Characteristic	S ⁵						
Total Gate Charge		Qg		-	110	-	nC
Gate-Source Charge		Qgs	VGS = -10V, VDS = -20V, ID= -20A	-	12.5	-	
Gate-Drain Charge		Qgd		-	23	-	1
Turn-on Delay Time		t d(on)		-	16.8	-	ns
Rise Time		tr	Vgs = -10V, Vdd = -20V,	_	10	_	
Turn-off Delay Time		t d(off)	$R_G = 3\Omega$, $I_D = -20A$	-	65	_	
Fall Time		tr	1	-	17	_	
Body Diode Reverse Recovery Time		t _{rr}			42	_	ns
Body Diode Reverse Recovery Charge		Qrr	_ I⊧ = -20A, dl/dt= 100A/μs	_	29	_	nC
Drain-Source Body Dio	de Characteris	tics	1]	1	1	L
Diode Forward Voltage ⁴		Vsd	Is = -20A, Vgs = 0V	-	_	-1.2	V
Continuous Source Current	Tc=25°C	Is			_	-80	A

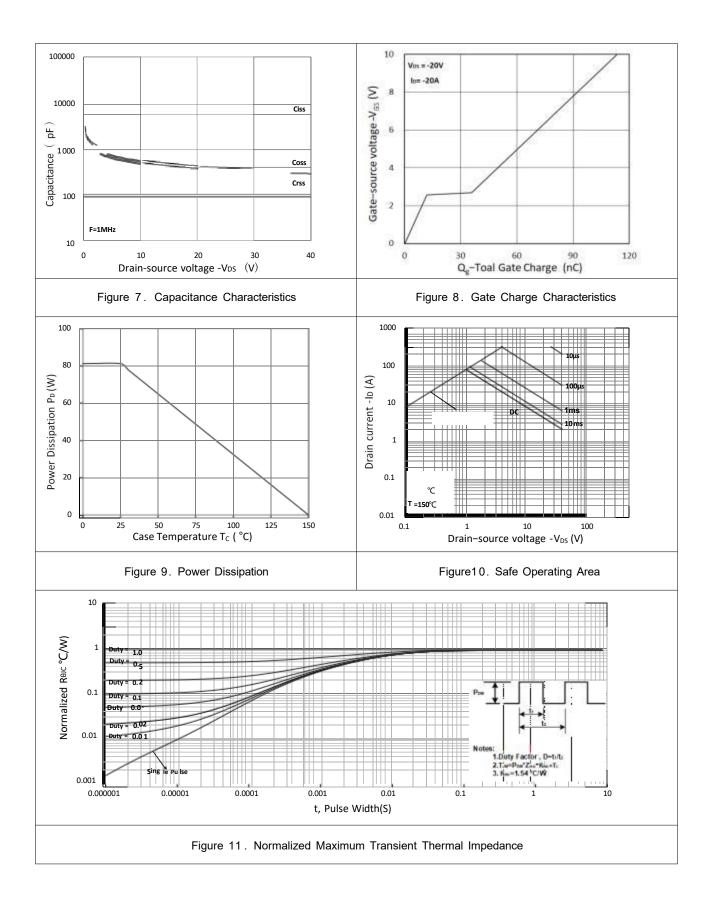
Notes:



Typical Characteristics

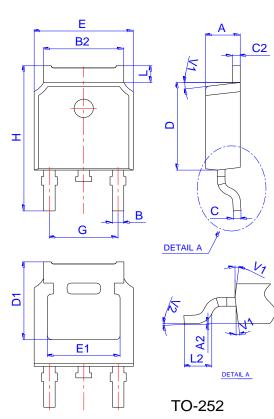






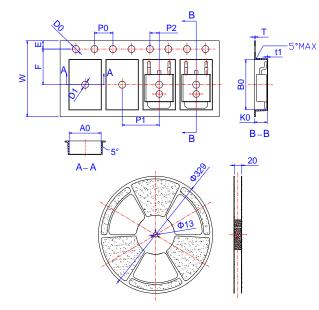


Package Mechanical Data



	Dimensions					
Ref.	Millimeters			Inches		
	Min.	Тур.	Max.	Min.	Тур.	Max.
A	2.10		2.50	0.083		0.098
A2	0		0.10	0		0.004
В	0.66		0.86	0.026		0.034
B2	5.18		5.48	0.202		0.216
С	0.40		0.60	0.016		0.024
C2	0.44		0.58	0.017		0.023
D	5.90		6.30	0.232		0.248
D1	5.30REF		0.209REF			
E	6.40		6.80	0.252		0.268
E1	4.63			0.182		
G	4.47		4.67	0.176		0.184
Н	9.50		10.70	0.374		0.421
L	1.09		1.21	0.043		0.048
L2	1.35		1.65	0.053		0.065
V1		7°			7°	
V2	0°		6°	0°		6°

Reel Spectification-TO-252



	Dimensions					
Ref.	Millimeters			Inches		
	Min.	Тур.	Max.	Min.	Тур.	Max.
W	15.90	16.00	16.10	0.626	0.630	0.634
Е	1.65	1.75	1.85	0.065	0.069	0.073
F	7.40	7.50	7.60	0.291	0.295	0.299
D0	1.40	1.50	1.60	0.055	0.059	0.063
D1	1.40	1.50	1.60	0.055	0.059	0.063
P0	3.90	4.00	4.10	0.154	0.157	0.161
P1	7.90	8.00	8.10	0.311	0.315	0.319
P2	1.90	2.00	2.10	0.075	0.079	0.083
A0	6.85	6.90	7.00	0.270	0.271	0.276
B0	10.45	10.50	10.60	0.411	0.413	0.417
K0	2.68	2.78	2.88	0.105	0.109	0.113
Т	0.24		0.27	0.009		0.011
t1	0.10			0.004		
10P0	39.80	40.00	40.20	1.567	1.575	1.583



Flow (wave) soldering (solder dipping)

Product	Peak Temperature	Dipping Time
Pb device	245℃±5 ℃	5sec ± 1sec
Pb-Free device	260 ℃+0/-5℃	5sec ± 1sec



This integrated circuit can be damaged by ESD UniverChip Corporation recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedure can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

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